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Fig.1.

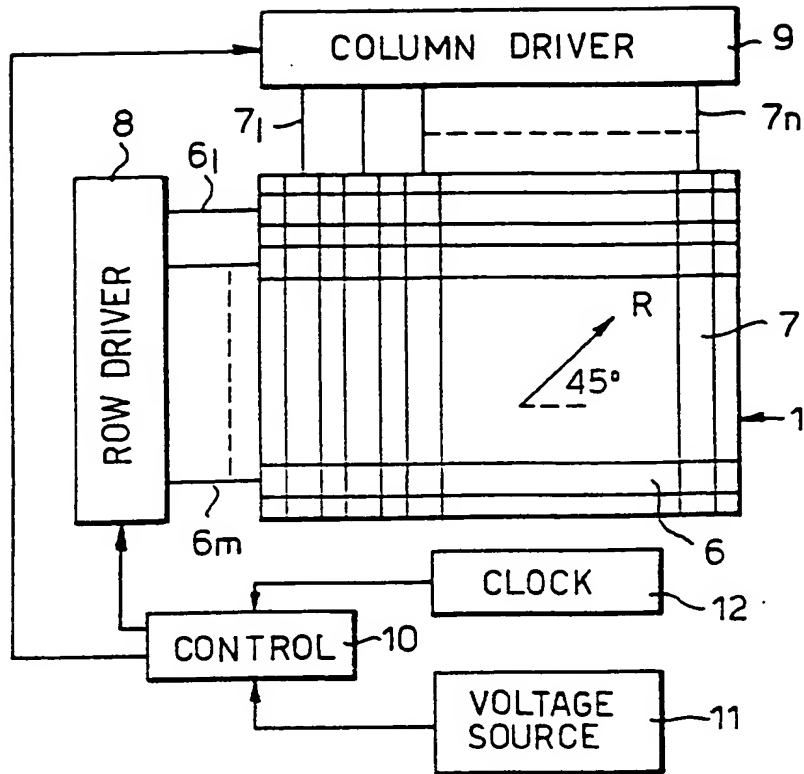
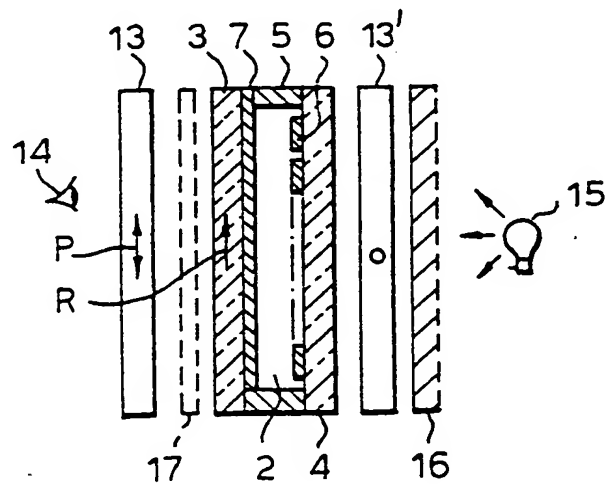


Fig.2.



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Fig.3.

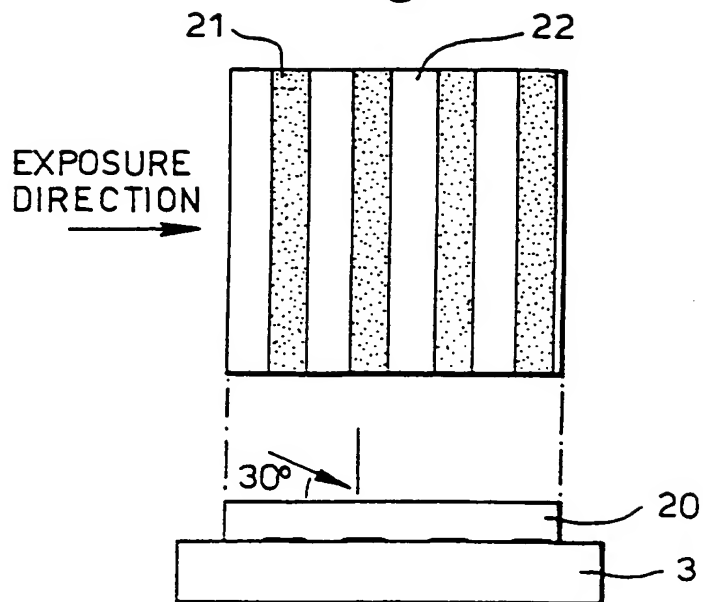


Fig.4.

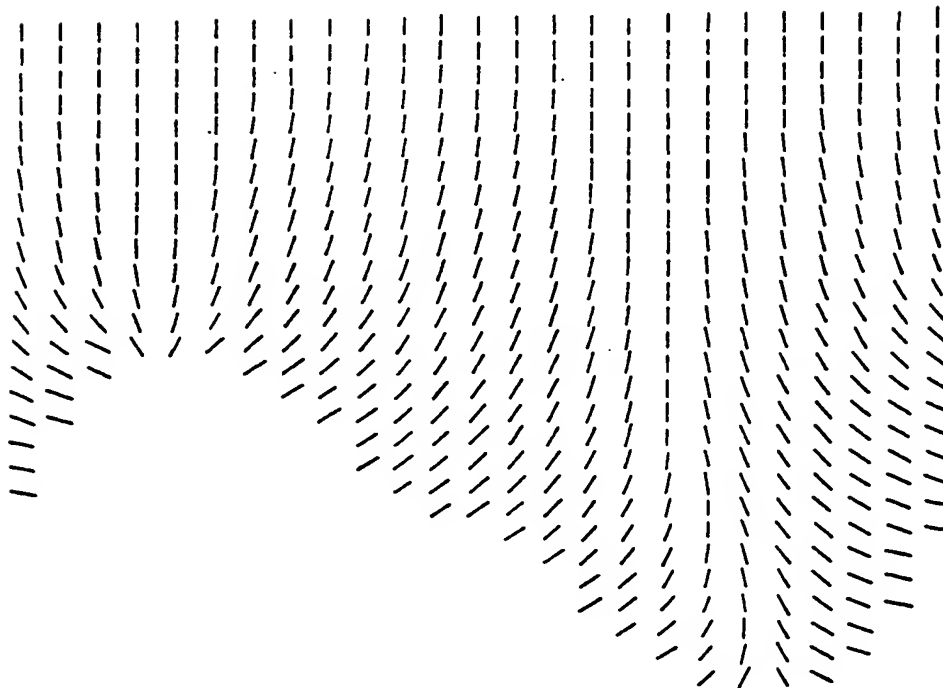


Fig.5.



Fig.6.

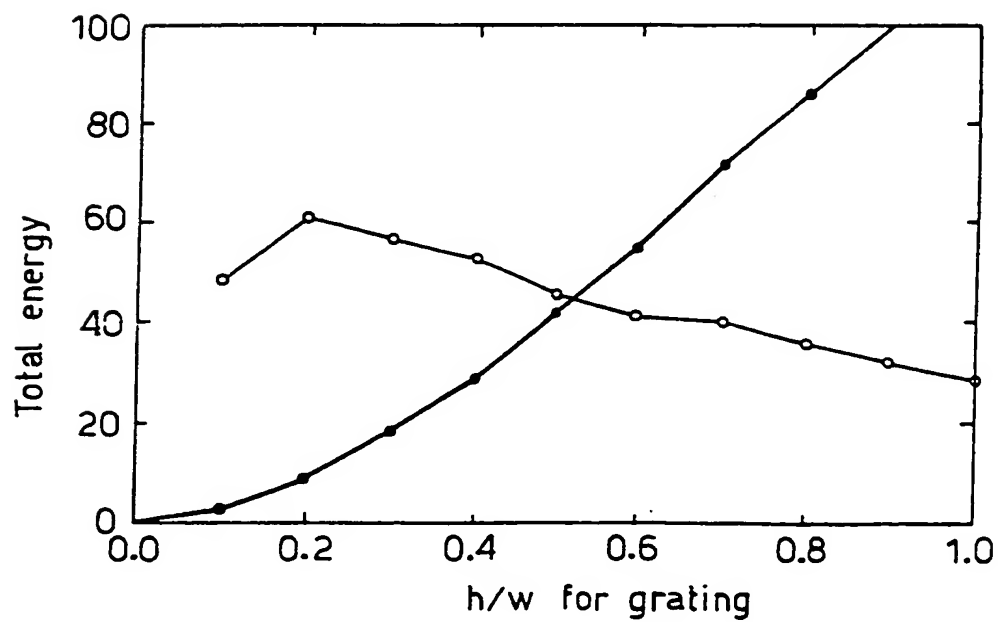


Fig.7 a.

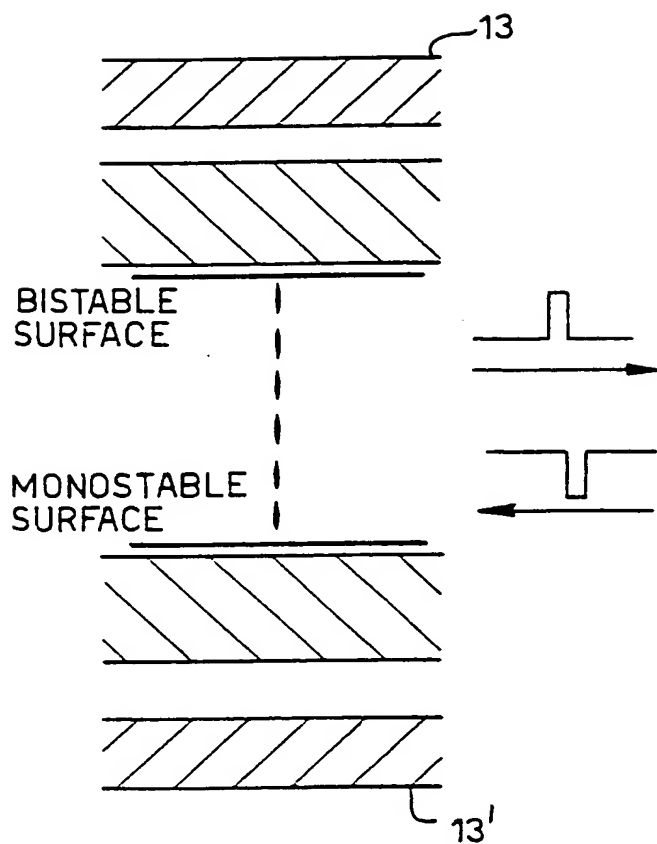
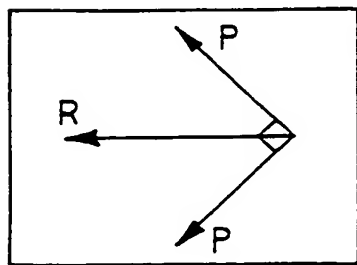
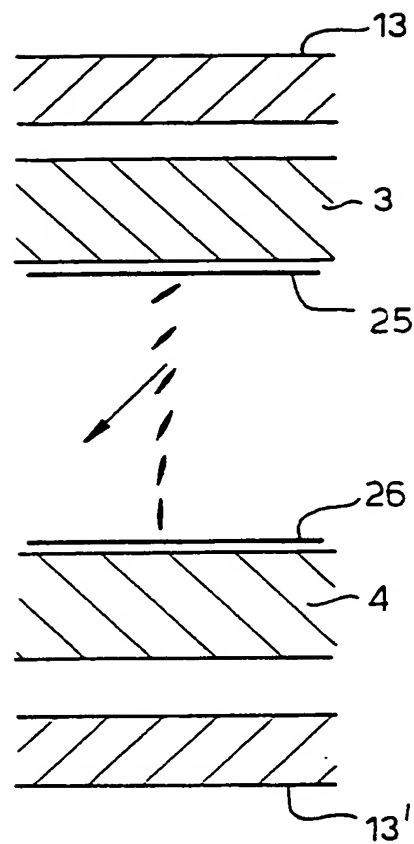


Fig.7b.



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Fig.8.

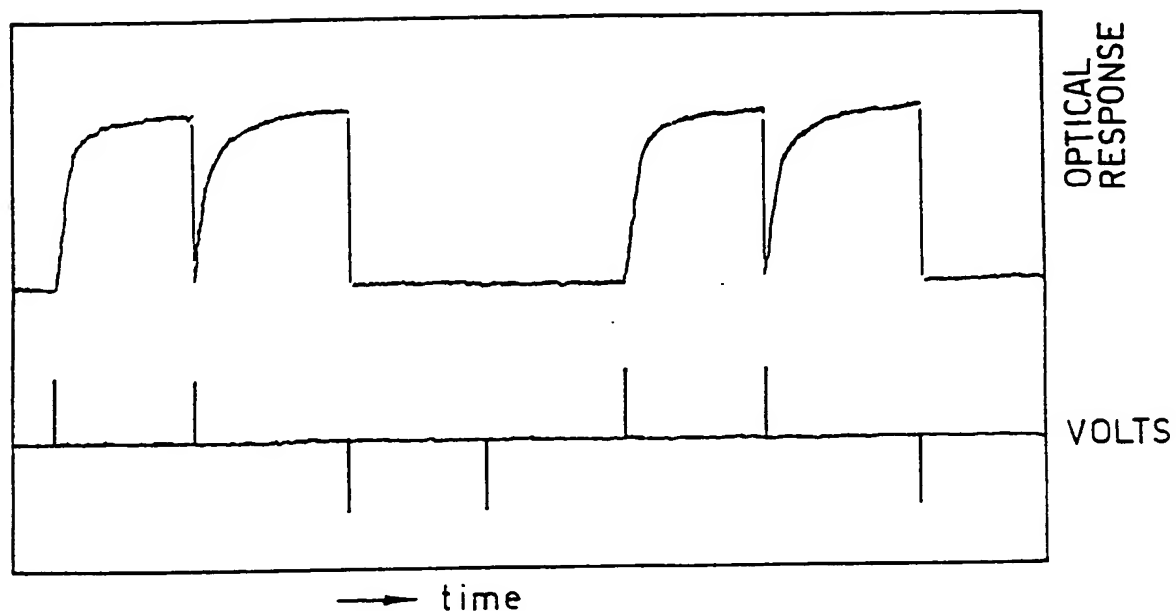


Fig.10a.

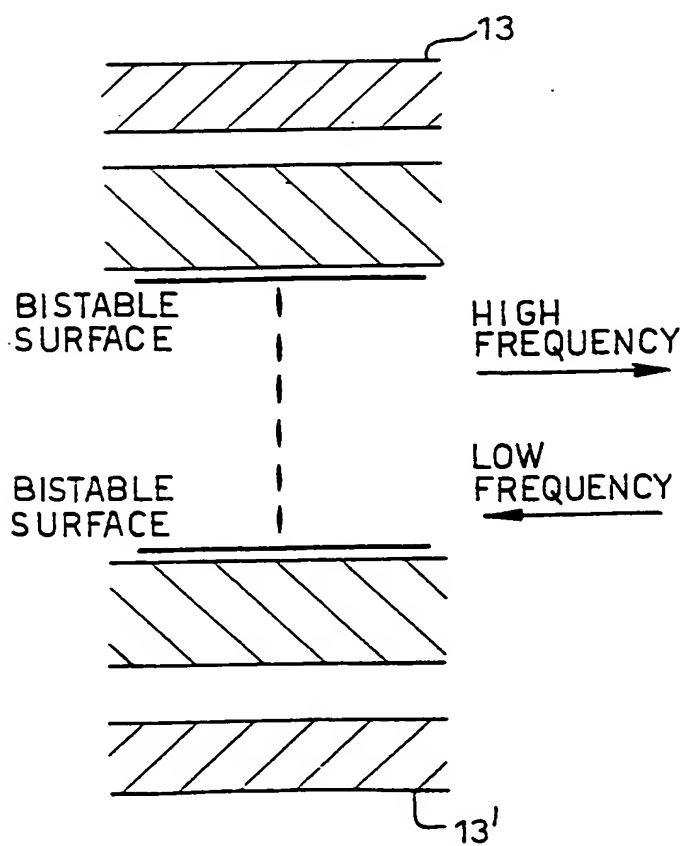


Fig.10b.

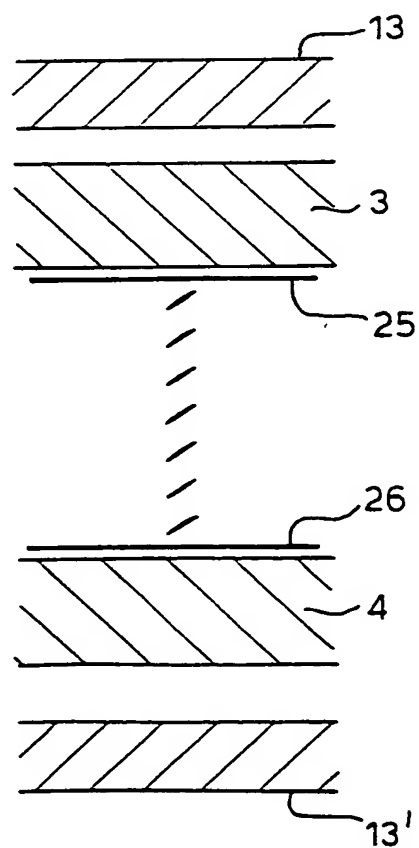
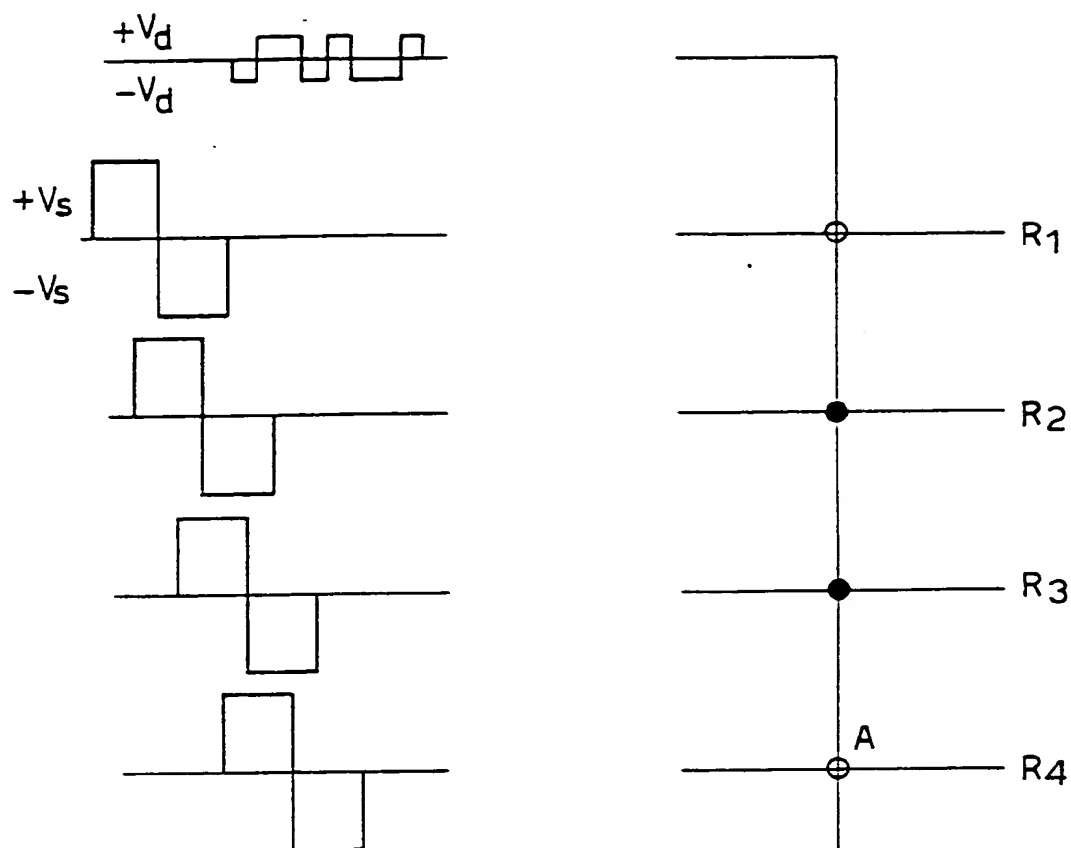
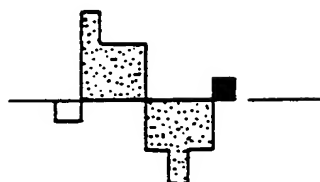


Fig.9.



RESULTANT
VOLTAGE
AT PIXEL A



■ BLANKING PULSE, OPENS
LINE FOR ADDRESSING

■ SELECTION PULSE

□ RESTORES dc BALANCE

Fig.11a.

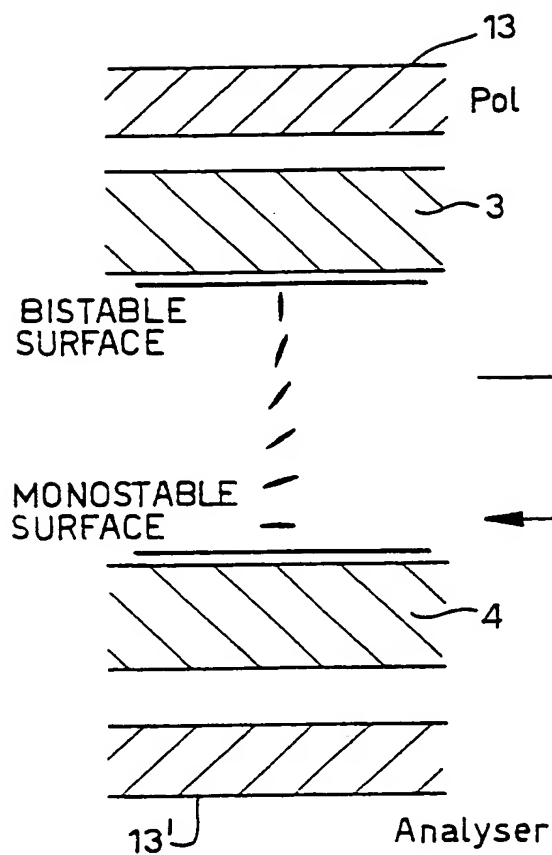


Fig.11b.

